Test Waveform Generator System
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Hardware status:
- Packaging the two-channels test system
  a) One Xilinx X2S150 FPGA Test Card (Digital)
  b) One AD9765 Test Card (Digital to Analog Conversion)
  c) Two AD8138 Card (Amplification/Filtering)

Software and Firmware status:
- Interface for the Xilinx X2S150 FPGA Test Card (Digital)
  a) Serial (RS232) interface completed/tested.
  b) GUI based on Microsoft Excel to control and change the waveform generated by the two-channel test system.

TWG Documentation:
http://www-ese.fnal.gov/D0Cal_TWG/
update in progress