

MI BPM Project
MI BPM TB Control Module Status Report March 7th, 2006, 9:30am

No changes in activities since last report on February 28th, 2006.

MI BPM TB Control Module Production

Hardware status:

-) Working on a preliminary version of the “Functional Requirements Specification”.
-) Work in progress on the conceptual design of the production version of the Control Module.

Firmware status:

-) Conceptual work in progress together with hardware design. Coding work will start as soon as hardware architecture is finalized.

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Firmware status:

-) Conceptual work in progress together with Control Module (production version) design.
-) Work in progress on “non-critical” improvements.

MI BPM TB Control Module Prototype

Hardware status:

-) No activities

Firmware status (Avnet Xilinx card FPGA):

-) Work to test controller (production version) design ideas.

Document related to the Control Module are available on the web page:

http://www-ese.fnal.gov/MI_BPM_TB_CTL/

MI BPM Project
MI BPM TB Control Module Time Schedule as on February 28th, 2006

Week beginning on	Task 1	Task 2	Task 3	Task 4	Goals and decisions
February 27 th	Design				
March 6 th	Design	Schematic			Finalize design
March 13 th	Firmware	Schematic			
March 20 th	Firmware	Schematic review			Minor design changes
March 27 th	Firmware	PCB layout		Parts ordering	
April 3 rd	Firmware	PCB layout		Parts arrive	
April 10 th	Firmware	PCB layout	PCB quotes	Parts arrive	
April 17 th	Firmware	PCB manufacturing	Front panel design	Parts arrive	Finalize preliminary version of firmware.
April 24 th		Module assembly	Front panel manufacturing		1 st Module assembled and tested
May 1 st		Module assembly	Front panel manufacturing		May 1 st : Does module meets system requirements?
May 8 th		Module testing	Front panel manufacturing		
May 15 th					May 15 th : All Modules assembled and tested
May 22 nd					
May 29 th					