

**MI BPM Project**  
**MI BPM TB Controller Status Report February 14<sup>th</sup>, 2006, 9:30am**

**Hardware status**

**MI BPM\_TB Controller Prototype:**

- ) Worked on Subrack Controller prototype documentation.
- ) Uploaded MI-BPM-TB Subrack Controller Prototype manual  
(Beam-doc-2145-v0)
- ) Uploaded MI-BPM-TB Subrack Controller Prototype electrical schematic  
(Beam-doc-2144-v0)
- ) Documents are also available on the web page:  
[http://www-ese.fnal.gov/MI\\_BPM\\_TB\\_CTL/](http://www-ese.fnal.gov/MI_BPM_TB_CTL/)
- ) Started conceptual design of an improved version of the Subrack Controller.

**Firmware status:**

**MI BPM\_TB Firmware**

- ) As previously reported work in progress on “non-critical” improvements.

**MI BPM\_TB Controller Prototype Firmware (Avnet Xilinx card FPGA)**

- ) As previously reported work in progress on “non-critical” improvements.