

## 7.7.1 Pin Assignments For The J1/P1 Connector

Table 7-1 provides signal names for the J1/P1 connector pins. (The 96 pin connector consists of three rows of pins labeled rows a, b, and c. The 160 pin connector has five rows labeled z, a, b, c and d.)

Table 7 - 1 J1/P1 Pin Assignments

Pin Number	Row z Signal Mnemonics	Row a Signal Mnemonics	Row b Signal Mnemonics	Row c Signal Mnemonics	Row d Signal Mnemonics
1	Reserved	D00	BBSY*	D08	Reserved
2	GND	D01	BCLR*	D09	Reserved
3	Reserved	D02	ACFAIL*	D10	Reserved
4	GND	D03	BG0IN*	D11	Reserved
5	Reserved	D04	BG0OUT*	D12	Reserved
6	GND	D05	BG1IN*	D13	Reserved
7	Reserved	D06	BG1OUT*	D14	Reserved
8	GND	D07	BG2IN*	D15	Reserved
9	Reserved	GND	BG2OUT*	GND	Reserved
10	GND	SYSCLK	BG3IN*	SYSFAIL*	Reserved
11	Reserved	GND	BG3OUT*	BERR*	Reserved
12	GND	DS1*	BR0*	SYSRESET*	Reserved
13	Reserved	DS0*	BR1*	LWORD*	Reserved
14	GND	WRITE*	BR2*	AM5	Reserved
15	Reserved	GND	BR3*	A23	Reserved
16	GND	DTACK*	AM0	A22	Reserved
17	Reserved	GND	AM1	A21	Reserved
18	GND	AS*	AM2	A20	Reserved
19	Reserved	GND	AM3	A19	Reserved
20	GND	IACK*	GND	A18	Reserved
21	Reserved	IACKIN*	SERA	A17	Reserved
22	GND	IACKOUT*	SERB	A16	Reserved
23	Reserved	AM4	GND	A15	Reserved
24	GND	A07	IRQ7*	A14	Reserved
25	Reserved	A06	IRQ6*	A13	Reserved
26	GND	A05	IRQ5*	A12	Reserved
27	Reserved	A04	IRQ4*	A11	Reserved
28	GND	A03	IRQ3*	A10	Reserved
29	Reserved	A02	IRQ2*	A09	Reserved
30	GND	A01	IRQ1*	A08	Reserved
31	Reserved	-12V	+5VSTDBY	+12V	Reserved
32	GND	+5V	+5V	+5V	Reserved