

Software for the DSP-based Trigger Prototype board.

This is the list of software tasks that we made during our meeting of September 26. The people assigned to work in a specific task are shown in the task list.

(Mike Wang, Jin-Yuan Wu) DSP software: - DSP BIOS

- DSP Applications

(Mike Wang, Gustavo Cancelo) Buffer Manager FPGA: - PTA Interface

- DSP Interface
- FIFO Interface

(Greg Deuerling) HPI-BSP FPGAs:

- Firmware
- PTSM and GLSM Interface
- Hitachi Interface
- HPI-BSP Interfaces

(Greg Deuerling) FLASH card CPLD:

- FLASH card Interface
- Hitachi Interface
- JTAG Interface

(Xiaonan Li) Arcnet software:

- PTSM and GL1 Arcnet software
- Hitachi Arcnet software

(David Berg) Hitachi software: - CPLD Interface

- HPI-BSP Interfaces
- Command, Status and Histogram handling software

(no names assigned yet) - PTA software for Data In and Out

- User Interface (command line)
- Software support tasks