

For stage two::

Overall:

- How different/similar are the muon trigger needs? Where can we share tasks and how can we partition tasks to make effective use of both groups?

FPGA Pixel processor:

- What sorting and tagging can be done here in order to simplify later processing?
- What degree of cluster finding processing can we afford?
- How do we select the best programmable chip architecture? Do there need to be specific simulations just for this selection process?

Track Finder & Processor Farm:

- What simulations need to be run to winnow candidate algorithms?
- What prototype tests need to be run to validate the processor simulations and how much prototyping needs to be done to support this?
- How do we select the processor hardware
- What is the operating environment? (NT, Linux, VxWorks)
- Should we separate the track finding and the vertex finding or make cuts on the segments?

Cut from Ed's List:

- Milestones & schedules: What are your milestones and schedule leading up to the February, 2002 Baseline review?

For the experiment::

Overall:

- Where physically do the pieces of the trigger need to be?
- What fault tolerance needs to be built in?

There are some points on Ed's list that will be discussed.