SNAP

PMC Firmware for controlling Flash Memory RAD Test Board
When LVDS_TRX_DE is '1', then the LVDS Transceiver on the Memory Board is driving signals to the PMC.

The equivalent equation is:
\[
LVDS_TRX_DE = TR_LVDS
\]

Delay is to account for bus turnaround time and avoid bus contention.
Delay is determined by DelayCount(15:0) value * DelayCntrCLK period

~5.24m sec

When IO_Direction = '1', PMC is receiving.
When IO_Direction = '0', PMC is driving.

This is emulating what is on the memory board.

Desired waveforms.