Firmware Type = SNAP Memory Board
Firmware Rev = 1.0

SNAP
PMC Firmware for controlling Flash Memory RAD Test Board

pmcregisters

Delay Count = 0xFFFF (~5.24m sec)

VCC
GND

Date: Sheet of
Name:
Title:

July 21, 2003
Bradley Hall - SNAP Memory Test

PMC top level. JN1 IO and Registers.
When LVDS_TRX_DE is '1', then the LVDS Transceiver on the Memory Board is driving signals to the PMC.

The equivalent equation is:
$$\text{LVDS\_TRX\_DE} = !\text{ALE\_LVDS}$$

This is emulating what is on the memory board.

Desired waveforms.

$\text{Delay}$ is determined by $\text{DelayCount}(15:0)$ value $\times$ $\text{DelayCnt\_CLK}$ period

-5.24m sec