Student's Name

Date

Subject

Instructor's Name

REWORK FOR J7 (Logic Analyzer Header) PMC V1.8 10/10/1

- Discovered problem w/ Logic Analyzer header in both PDA and PMC Card:

  - Pin 3 should be open (its connected to GND)
  - Probe GND return (5 in the center) should be connect to GND (its now unconnected)

- Fixed above problem on PMC by:

  - lift Pin 3 on J7 (HP Logic analyzer header)
  - wire GND return signal in center of J7 connector to GND