Reset Distribute on J4 (pin 1 = positive) Termination R16.

LEDs turn 'on' with logic '0'
Title: PMC top level. Misc inputs and XTAL in Counter.
Name: Bradley Hall - PreFPIX2tb Test Beam 2002
Date: November 25, 2002
Sheet: 4 of 8
PMC top level: Clock Selection and Trigger.

Title: Bradley Hall - PreFPIX2tb Test Beam 2002
Name: Bradley Hall
Date: November 25, 2002

Sheet of 8

Title: PMC top level: Clock Selection and Trigger.
Name: Bradley Hall - PreFPIX2tb Test Beam 2002
Date: November 25, 2002
Sheet 5 of 8