Had to make copy of `mux2to1_16bits` that is used in the PMC registers block. Caused corrupt register read/writing otherwise.
Title: PMC top level. Periphery I/O and LEDs
Name: Bradley H. - PreFPIX2 Test Beam 2002
Date: November 25, 2002

Reset Distribute on J4 (pin 1 = positive) Termination R16.

LEDs turn 'on' with logic '0'
NIM Inputs

Fake Data Clock Selection M8_1E

Demux X1

Data Valid Signal for Fake Data

Fake Data Generator CB16CE

Clocks for Fake Data

Fake_Data_Out(15:0)

SlowClkGen(15:0)

COUNTCLK

COUNT_EN

IBUF

IBUFG

IBUF

IBUF

Sel = 000  -> 25 MHz
Sel = 001  -> 12.5 MHz
Sel = 010  -> 6.25 MHz
Sel = 011  -> 3.13 MHz
Sel = 100  -> 1.56 MHz
Sel = 101  -> 781 kHz
Sel = 110  -> 391 kHz
Sel = 111  -> 195 kHz

Frequencies shown assuming 50MHz oscillator (X1)

Fake_Data_Out(31:0)

Fake_Data_Valid

VCC

M8_1E

D0

D1

D2

D3

O

D4

D5

D6

D7

S0

S1

S2

E

B

C

D

Bradley Hall - PreFPIX2i Test Beam 2002
November 25, 2002

Title:  PMC top level. Misc inputs and XTAL in Counter.
Name:  Bradley Hall - PreFPIX2i Test Beam 2002
Date:  November 25, 2002
Sheet 4 of 8
Title: PMC top level. Clock Selection and Trigger.
Name: Bradley Hall - PreFPIX2i Test Beam 2002
Date: November 25, 2002
Sheet 5 of 8