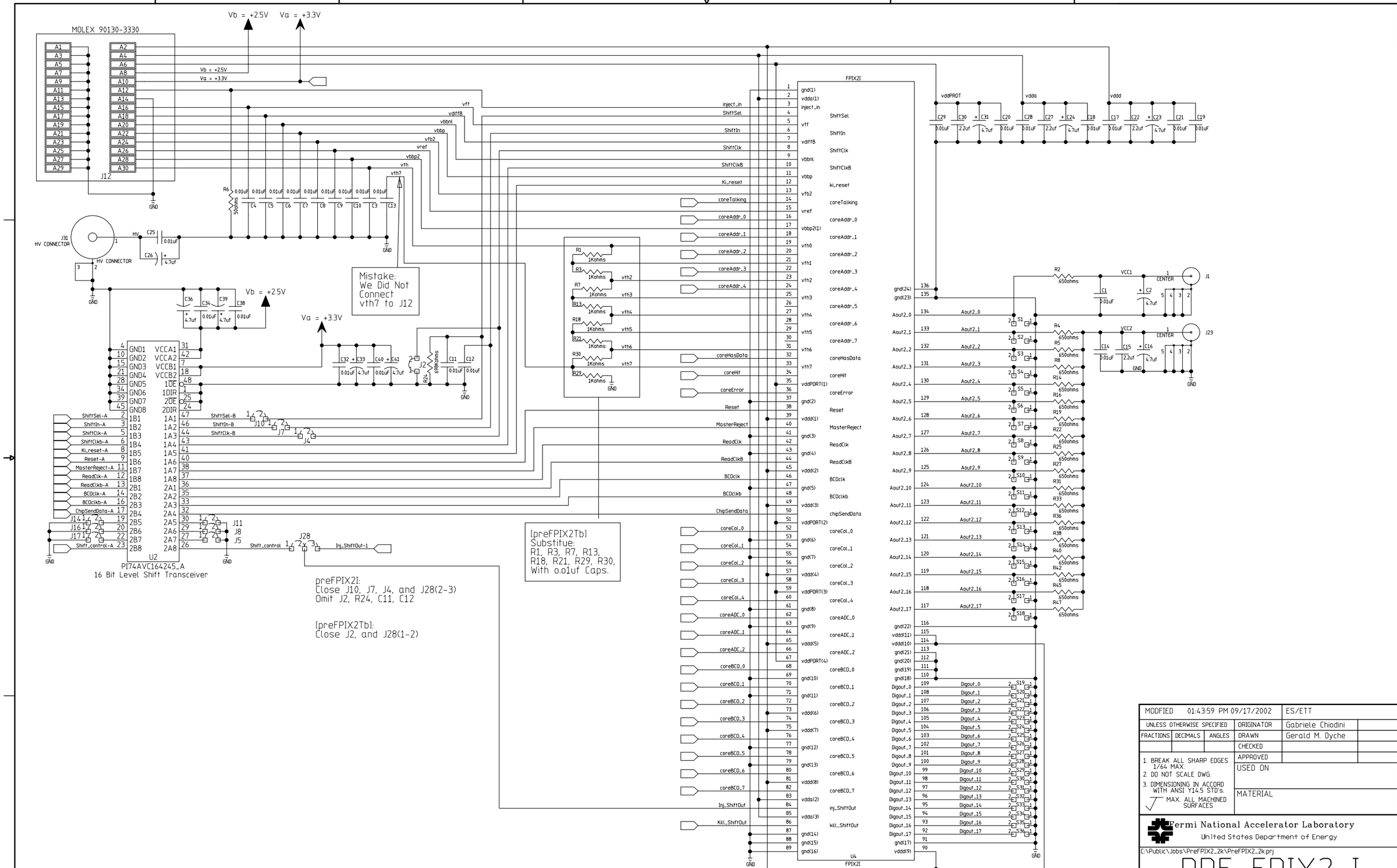

MODFIED 01:43:59 PM 09/17/2002			ES/ETT	
UNLESS OTHERWISE SPECIFIED			ORIGINATOR	Gabriele Chiodini
FRACTIONS	DECIMALS	ANGLES	DRAWN	Gerald M. Dyche
			CHECKED	
1. BREAK ALL SHARP EDGES 1/64 MAX.			APPROVED	
2. DO NOT SCALE DWG.			USED ON	
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD's.			MATERIAL	
MAX. ALL MACHINED SURFACES				

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United States Department of Energy

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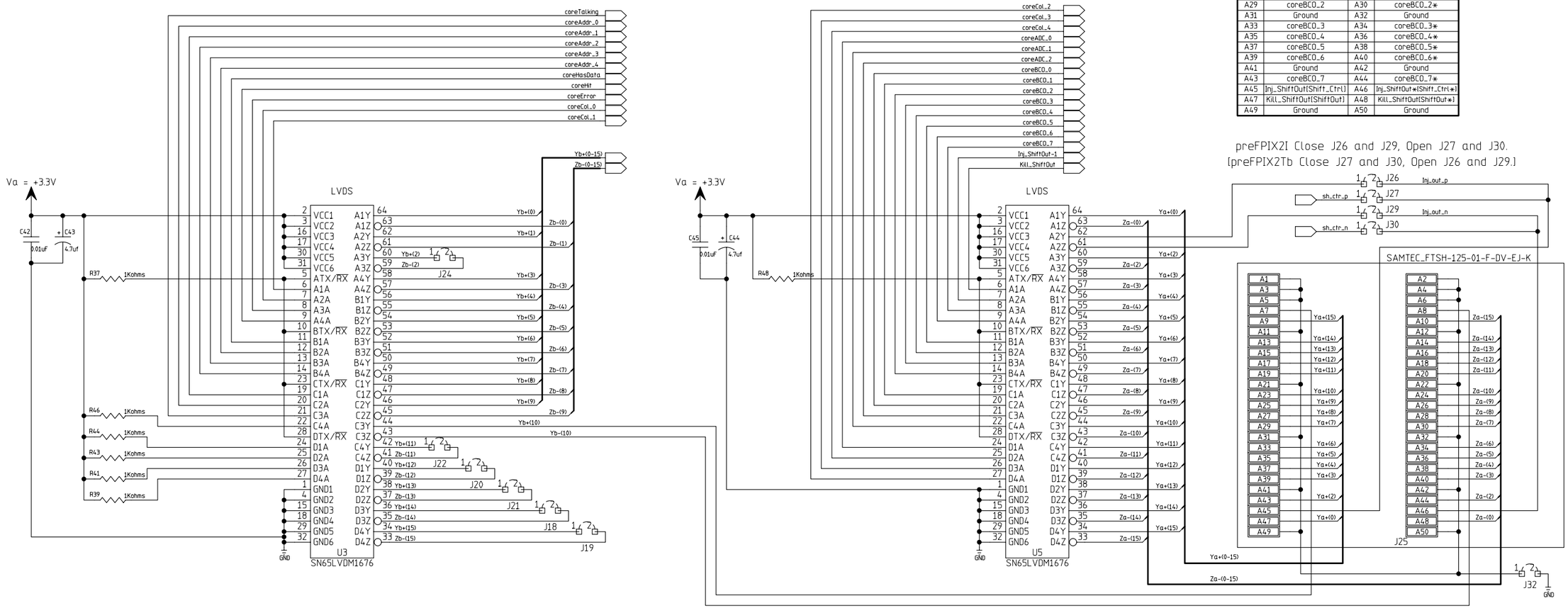
PRE FPIX2_I

SIZE	SCALE	FILMED	DRAWING NUMBER	PAGE 01 of 03	REV
D	1:1				A

H=8ND, V=4LA

preFPIX2I [preFPIX2Tb]

J25			
Pin #	Signal Name	Pin #	Signal Name
A1	Ground	A2	Ground
A3	Ground	A4	Ground
A5	Ground	A6	Ground
A7	coreTalking	A8	coreTalking*
A9	coreCol_2	A10	coreCol_2*
A11	Ground	A12	Ground
A13	coreCol_3	A14	coreCol_3*
A15	coreCol_4	A16	coreCol_4*
A17	coreADC_0	A18	coreADC_0*
A19	coreADC_1	A20	coreADC_1*
A21	Ground	A22	Ground
A23	coreADC_2	A24	coreADC_2*
A25	coreBCO_0	A26	coreBCO_0*
A27	coreBCO_1	A28	coreBCO_1*
A29	coreBCO_2	A30	coreBCO_2*
A31	Ground	A32	Ground
A33	coreBCO_3	A34	coreBCO_3*
A35	coreBCO_4	A36	coreBCO_4*
A37	coreBCO_5	A38	coreBCO_5*
A39	coreBCO_6	A40	coreBCO_6*
A41	Ground	A42	Ground
A43	coreBCO_7	A44	coreBCO_7*
A45	Inj_ShiftOut(Shift_Ctrl)	A46	Inj_ShiftOut*(Shift_Ctrl*)
A47	Kill_ShiftOut(ShiftOut)	A48	Kill_ShiftOut(ShiftOut*)
A49	Ground	A50	Ground



LVDS Transmitters

MODIFIED 01:38:37 PM 09/17/2002			ES/ETT
UNLESS OTHERWISE SPECIFIED		ORIGINATOR	Gabriele Chiodini
FRACTIONS	DECIMALS	ANGLES	DRAWN Gerald M. Dyche
			CHECKED
			APPROVED

1. BREAK ALL SHARP EDGES 1/64 MAX.
2. DO NOT SCALE DWG.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD's.

√ MAX. ALL MACHINED SURFACES

USED ON

MATERIAL

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PRE FPIX2_I

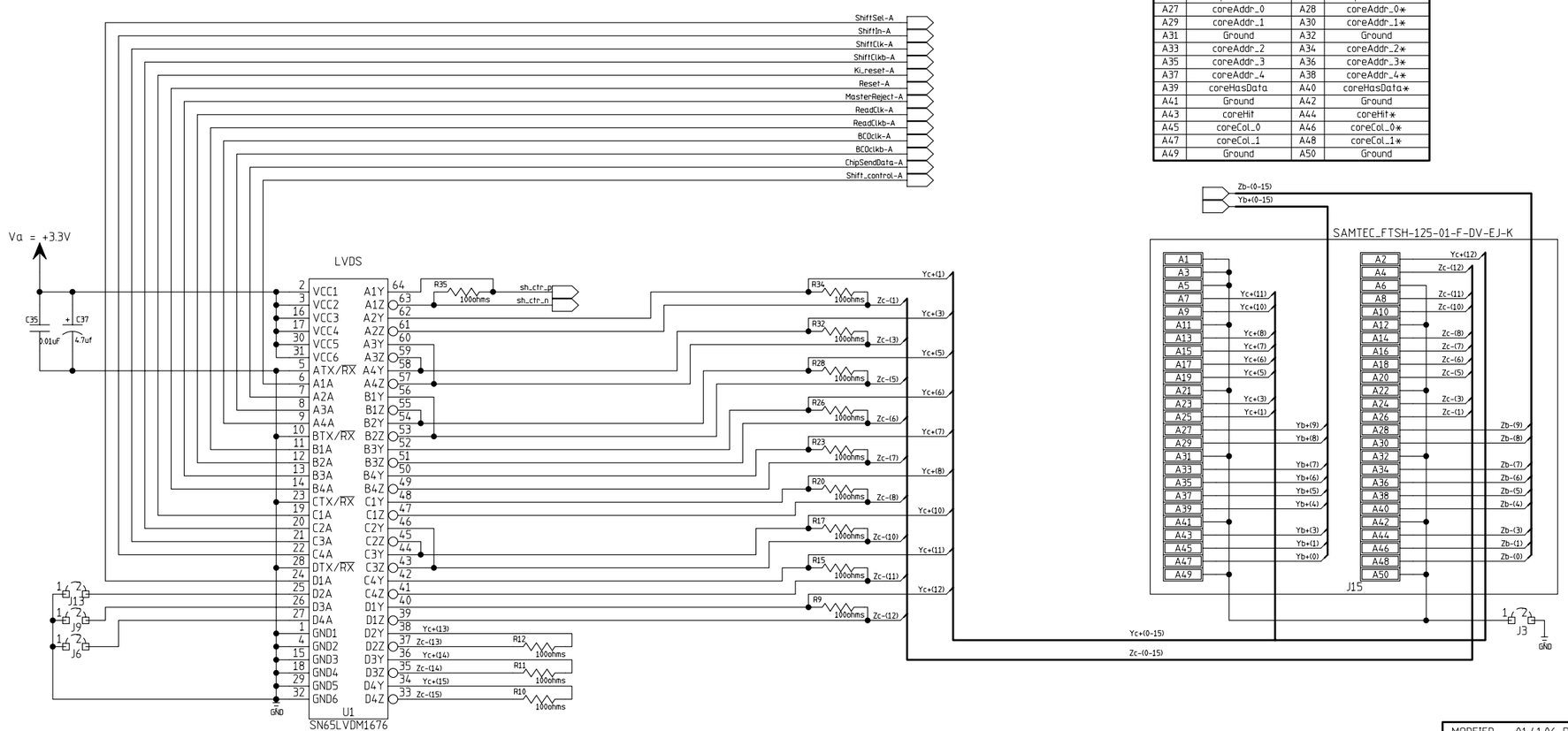
SIZE	SCALE	FILMED	DRAWING NUMBER	PAGE 02 of 03	REV
D	1:1				A

H=BND; V=4LA

preFPIX2I [preFPIX2Tb]

J15

Pin #	Signal Name	Pin #	Signal Name
A1	Ground	A2	ShiftSel(Not Used)
A3	Ground	A4	ShiftSel*(Not Used)
A5	Ground	A6	Ground
A7	ShiftIn(Not Used)	A8	ShiftIn*(Not Used)
A9	ShiftClk(ShiftIn*)	A10	ShiftClk(ShiftIn)
A11	Ground	A12	Ground
A13	Ki_reset(Pr_reset)	A14	Ki_reset*(Pr_reset*)
A15	Reset	A16	Reset*
A17	MasterReject	A18	MasterReject*
A19	ReadClk	A20	ReadClk*
A21	Ground	A22	Ground
A23	BCOClk	A24	BCOClk*
A25	ChipSendData	A26	ChipSendData*
A27	coreAddr_0	A28	coreAddr_0*
A29	coreAddr_1	A30	coreAddr_1*
A31	Ground	A32	Ground
A33	coreAddr_2	A34	coreAddr_2*
A35	coreAddr_3	A36	coreAddr_3*
A37	coreAddr_4	A38	coreAddr_4*
A39	coreHasData	A40	coreHasData*
A41	Ground	A42	Ground
A43	coreHit	A44	coreHit*
A45	coreCol_0	A46	coreCol_0*
A47	coreCol_1	A48	coreCol_1*
A49	Ground	A50	Ground



LVDS Receiver

MODFIED 01:41:06 PM 09/17/2002			ES/ETT	
UNLESS OTHERWISE SPECIFIED			ORIGINATOR	Gabriele Chiodini
FRACTIONS	DECIMALS	ANGLES	DRAWN	Gerald M. Dyche
			CHECKED	
			APPROVED	

1. BREAK ALL SHARP EDGES 1/64 MAX.
2. DO NOT SCALE DWG.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD's.

√ MAX. ALL MACHINED SURFACES

USED ON

MATERIAL

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PRE FPIX2_I

SIZE	SCALE	FILMED	DRAWING NUMBER	PAGE 03 of 03	REV
D	1:1				A

H=BND; V=4LA