FPIX1 Software Pass-Through Rev 1.3

The only difference between Rev 1.2 and Rev 1.3 is DATA_VALID is a single ended input. R53 must be removed. This version created to accommodate a faulty DATA_VALID driver on the pixel module.

Use for both FPIX1 test beam '02 board and for FPIX1 Pixel Module.

Use SAMTEC C and D for FPIX1 test beam '02 board
Use SAMTEC C and A for Pixel Module

Jump J5 pin 2 to J5 pin 1 to select Module

Title: SW Passthru to New FPIX1 Inner Board, JN1/JN2 signals.
Name: Bradley Hall.
Date: April 8, 2003
**SW Pass thru to SAMTEC C and D.**

For Module connect neg side of R53 to bias level to emulate differential input. FPIX1 modules with 5 chips only has a DataValid swing of ~0 to ~1.3V.

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**Title:** SW Pass thru to SAMTEC C and D.

**Name:** Bradley Hall

**Date:** April 8, 2003
TTL_LV0 used to select Module or Test Beam FPIX1 board. TTL_LV0 is J5 pin 1. Jump J5 pin 1 to J5 pin 2 (GND) to select Module. Let J5 pin 1 float for Test Beam Board. There is an internal pull-up resistor on TTL_LV0.

Make sure resistor R28 is installed (0 to 100 Ohm).