Title: SW Passathru to Test Beam '02 FPIX0 Inner Board. Rev 1.0
Name: Bradley Hall. LVDS I/O on SAMTEC A and B.
Date: August 15, 2002

Sheet of Bradley Hall. LVDS I/O on SAMTEC A and B.

SAMTEC A LVDS Inputs

SAMTEC B LVDS Inputs and Outputs

Note that PMC Document signal Name GPIO_A1_N_CLK is samtec_n_a0, GPIO_A2_N is samtec_n_a1, and so on.

PMC Resistors are for LVDS termination. Only the LVDS Inputs should have their PMC resistor populated.
Signals 'CHIP.OR' and 'DACS' are active low. LEDs will turn on when OBUF drive logic '0'.