Connections between Frameworks, Master Clocks and D0 Trigger System

Common Signals Between All Geographic Sections (16 SLFs)

64-Pin Connector

1	+BOT_Mrkr	-BOT_Mrkr	2
3	+BX_Mrkr	-BX_Mrkr	4
5	+SyncG_Mrkr	-SyncG_Mrkr	6
7	+Cos_Mrkr	-Cos_Mrkr	8
9	+Spare Marker	-Spare Marker	10
11	+Reserved0	-Reserved0	12
13	+Reserved1	-Reserved1	14
15	+Reserved2	-Reserved2	16
17	+Curnttrn0	-Curnttrn0	18
19	+Curnttrn1	-Curnttrn1	20
21	+Curnttrn2	-Curnttrn2	22
23	+Curnttrn3	-Curnttrn3	24
25	+Curnttrn4	-Curnttrn4	26
27	+Curnttrn5	-Curnttrn5	28
29	+Curnttrn6	-Curnttrn6	30
31	+Curnttrn7	-Curnttrn7	32
33	+Curnttrn8	-Curnttrn8	34
35	+Curnttrn9	-Curnttrn9	36
37	+Curnttrn10	-Curnttrn10	38
39	+Curnttrn11	-Curnttrn11	40
41	+Curnttrn12	-Curnttrn12	42
43	+Curnttrn13	-Curnttrn13	44
45	+Curnttrn14	-Curnttrn14	46
47	+Curnttrn15	-Curnttrn15	48
49	+CurntBX0	-CurntBX0	50
51	+CurntBX1	-CurntBX1	52
53	+CurntBX2	-CurntBX2	54
55	+CurntBX3	-CurntBX3	56
57	+CurntBX4	-CurntBX4	58
59	+CurntBX5	-CurntBX5	60
61	+CurntBX6	-CurntBX6	62
63	+CurntBX7	-CurntBX7	64

64-Pin Connector

1	+L1A_Period	-L1A_Period	2
3	+Initialize	-Initialize	4
5	+L2_Decsn	-L2_Decsn	6
7	+Init_Ack	-Init_Ack	8
9	+GB_SynErr	-GB_SynErr	10
11	+IRQ	-IRQ	12
13	+L1Trn0	-L1Trn0	14
15	+L1Trn1	-L1Trn1	16
17	+L1Trn2	-L1Trn2	18
19	+L1Trn3	-L1Trn3	20
21	+L1Trn4	-L1Trn4	22
23	+L1Trn5	-L1Trn5	24
25	+L1Trn6	-L1Trn6	26
27	+L1Trn7	-L1Trn7	28
29	+L1Trn8	-L1Trn8	30
31	+L1Trn9	-L1Trn9	32
33	+L1Trn10	-L1Trn10	34
35	+L1Trn11	-L1Trn11	36
37	+L1Trn12	-L1Trn12	38
39	+L1Trn13	-L1Trn13	40
41	+L1Trn14	-L1Trn14	42
43	+L1Trn15	-L1Trn15	44
45	+L1BX0	-L1BX0	46
47	+L1BX1	-L1BX1	48
49	+L1BX2	-L1BX2	50
51	+L1BX3	-L1BX3	52
53	+L1BX4	-L1BX4	54
55	+L1BX5	-L1BX5	56
57	+L1BX6	-L1BX6	58
59	+L1BX7	-L1BX7	60
61	+Reserved3	-Reserved3	62
63	+Reserved4	-Reserved4	64

Return Signals to Frameworks

16-Pin Connectors

1	+GSL1B0	-GSL1B0	2
3	+GSL1B1	-GSL1B1	4
5	+GSL1B2	-GSL1B2	6
7	+GSL1B3	-GSL1B3	8
9	+GSL1B4	-GSL1B4	10
11	+GSL1B5	-GSL1B5	12
13	+GSL1B6	-GSL1B6	14
15	+GSL1B7	-GSL1B7	16

1	+GSL2B0	-GSL2B0	2
3	+GSL2B1	-GSL2B1	4
5	+GSL2B2	-GSL2B2	6
7	+GSL2B3	-GSL2B3	8
9	+GSL2B4	-GSL2B4	10
11	+GSL2B5	-GSL2B5	12
13	+GSL2B6	-GSL2B6	14
15	+GSL2B7	-GSL2B7	16

1	+SPARE1_0	-SPARE1_0	2
3	+SPARE1_1	-SPARE1_1	4
5	+SPARE1_2	-SPARE1_2	6
7	+SPARE1_3	-SPARE1_3	8
9	+SPARE1_4	-SPARE1_4	10
11	+SPARE1_5	-SPARE1_5	12
13	+SPARE1_6	-SPARE1_6	14
15	+SPARE1_7	-SPARE1_7	16

1	+SPARE2_0	-SPARE2_0	2
3	+SPARE2_1	-SPARE2_1	4
5	+SPARE2_2	-SPARE2_2	6
7	+SPARE2_3	-SPARE2_3	8
9	+SPARE2_4	-SPARE2_4	10
11	+SPARE2_5	-SPARE2_5	12
13	+SPARE2_6	-SPARE2_6	14
15	+SPARE2_7	-SPARE2_7	16

Common Signals between 16 Sections (2 SLFs)

34-Pin Connector

1	+L1L3NUM0	-L1L3NUM0	2
3	+L1L3NUM1	-L1L3NUM1	4
5	+L1L3NUM2	-L1L3NUM2	6
7	+L1L3NUM3	-L1L3NUM3	8
9	+L1L3NUM4	-L1L3NUM4	10
11	+L1L3NUM5	-L1L3NUM5	12
13	+L1L3NUM6	-L1L3NUM6	14
15	+L1L3NUM7	-L1L3NUM7	16
17	+L1L3NUM8	-L1L3NUM8	18
19	+L1L3NUM9	-L1L3NUM9	20
21	+L1L3NUM10	-L1L3NUM10	22
23	+L1L3NUM11	-L1L3NUM11	24
25	+L1L3NUM12	-L1L3NUM12	26
27	+L1L3NUM13	-L1L3NUM13	28
29	+L1L3NUM14	-L1L3NUM14	30
31	+L1L3NUM15	-L1L3NUM15	32
33			34

Signals for Individual SLFs

16-Pin Connectors

1	+L1ACCPT0	-L1ACCPT0	2
3	+L1ACCPT1	-L1ACCPT1	4
5	+L1ACCPT2	-L1ACCPT2	6
7	+L1ACCPT3	-L1ACCPT3	8
9	+L1ACCPT4	-L1ACCPT4	10
11	+L1ACCPT5	-L1ACCPT5	12
13	+L1ACCPT6	-L1ACCPT6	14
15	+L1ACCPT7	-L1ACCPT7	16

1	+L2ACCPT0	-L2ACCPT0	2
3	+L2ACCPT1	-L2ACCPT1	4
5	+L2ACCPT2	-L2ACCPT2	6
7	+L2ACCPT3	-L2ACCPT3	8
9	+L2ACCPT4	-L2ACCPT4	10
11	+L2ACCPT5	-L2ACCPT5	12
13	+L2ACCPT6	-L2ACCPT6	14
15	+L2ACCPT7	-L2ACCPT7	16

1	+L2REJ0	-L2REJ0	2
3	+L2REJ1	-L2REJ1	4
5	+L2REJ2	-L2REJ2	6
7	+L2REJ3	-L2REJ3	8
9	+L2REJ4	-L2REJ4	10
11	+L2REJ5	-L2REJ5	12
13	+L2REJ6	-L2REJ6	14
15	+L2REJ7	-L2REJ7	16

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