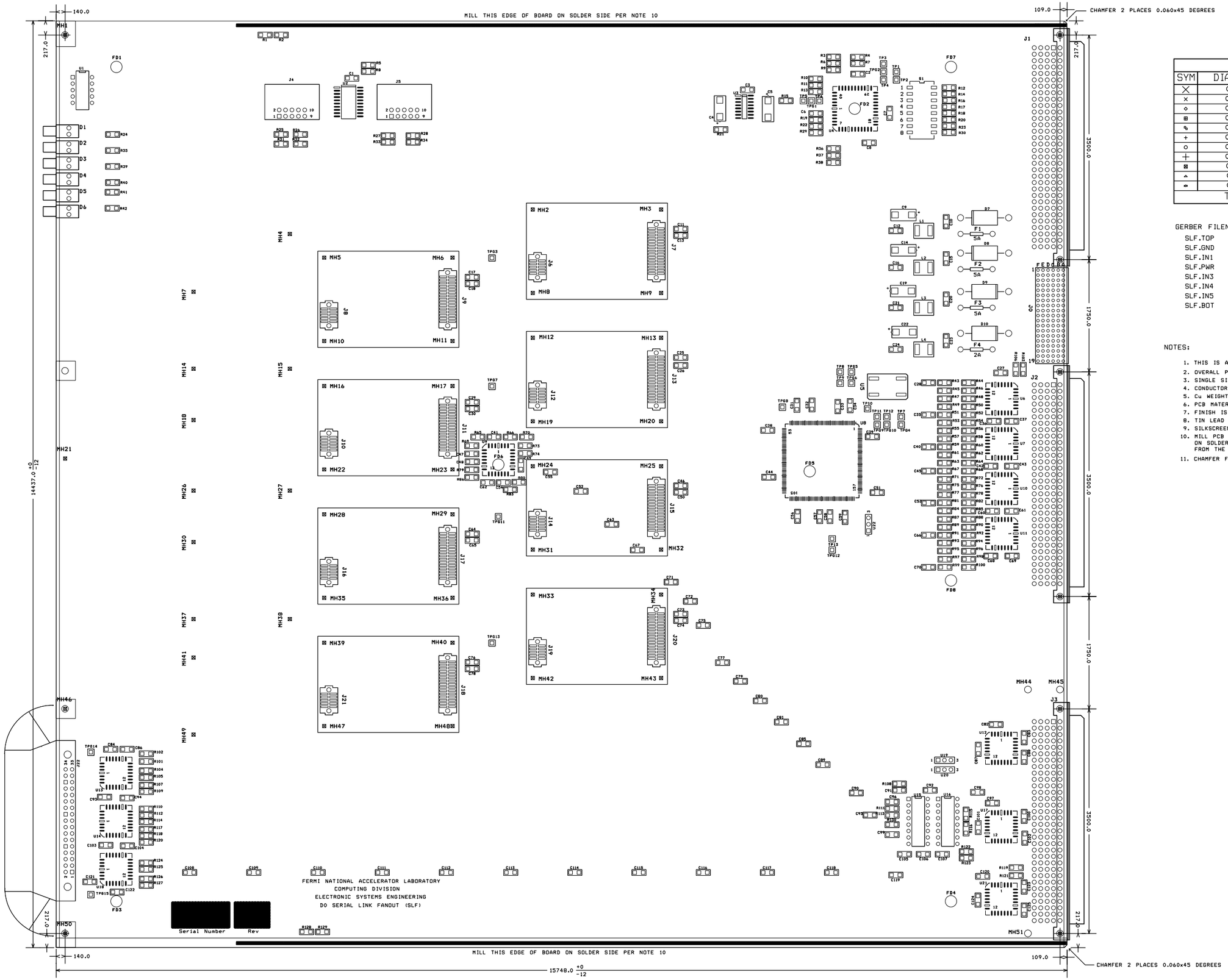

DRILL CHART				
SYM	DIAM	TOL	QTY	NOTE
╳	0.012		932	
×	0.025		114	
◇	0.034		42	
⊞	0.038		562	
⁂	0.042		29	
+	0.047		32	
○	0.055		8	
┼	0.080		8	
⊠	0.106		54	NON-PLATED
⌃	0.110		3	
⌖	0.116		2	NON-PLATED
TOTAL			1786	

GERBER FILENAME	BOARD LAYER STACKING ORDER
SLF.TOP	LAYER 1 = TOP TRACE
SLF.GND	LAYER 2 = GROUND PLANE
SLF.IN1	LAYER 3 = INNER TRACE 1
SLF.PWR	LAYER 4 = +5V POWER PLANE
SLF.IN3	LAYER 5 = +3.3V POWER PLANE
SLF.IN4	LAYER 6 = -5.2V POWER PLANE
SLF.IN5	LAYER 7 = -2V POWER PLANE
SLF.BOT	LAYER 8 = BOTTOM TRACE

NOTES:

1. THIS IS A 50 OHM CONTROLLED IMPEDANCE PCB
2. OVERALL PCB THICKNESS = 0.093 +/- 0.008 INCH
3. SINGLE SIDED 8 LAYER PCB WITH 3 SIGNAL LAYERS
4. CONDUCTOR WIDTH IS 8 MILS WITH 6 MILS SPACING
5. Cu WEIGHT 1oz ON ALL LAYERS
6. PCB MATERIAL IS FR4
7. FINISH IS SOLDER MASK OVER BARE COPPER
8. TIN LEAD PLATING WITH HOT AIR LEVELING
9. SILKSCREEN ON TOP LAYER COLOR WHITE
10. MILL PCB EDGE TO A THICKNESS OF 0.063 +/- 0.008 INCH ON SOLDER SIDE OF BOARD FOR A DISTANCE OF 0.098 INCH FROM THE TOP AND BOTTOM OF THE BOARD
11. CHAMFER FRONT 2 CORNERS OF BOARD 0.060" X 45 DEGREES