TOP LAYER 1 (VRB.TOP)

BOTTOM LAYER (VRB.BOT)

GROUND PLANE (VRB.GND)

LAYER SPACING

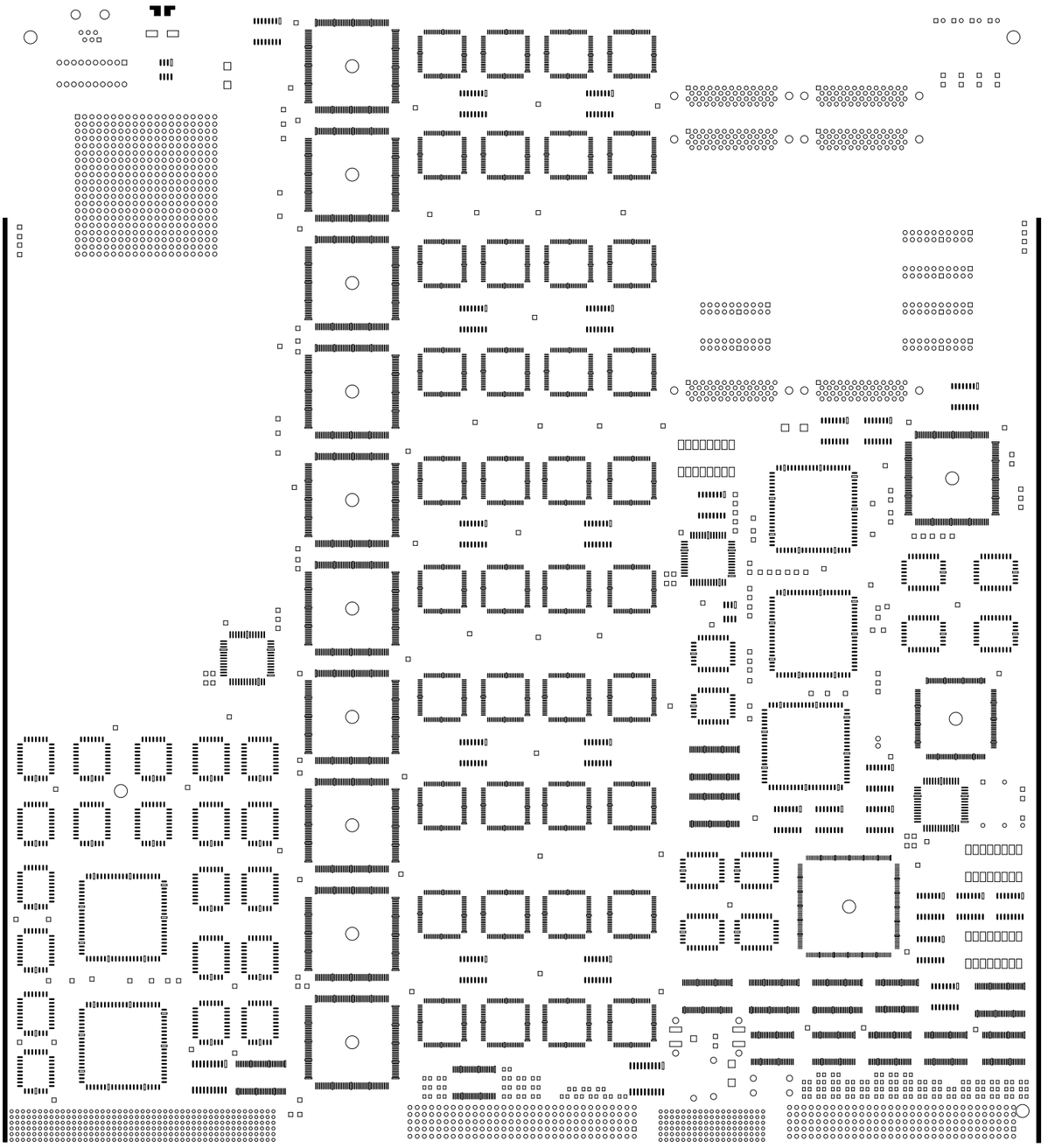
POWER PLANE (VRB.PWR)

INNER TRACE 1 (VRB.IN1)

INNER TRACE 2 (VRB.IN2)

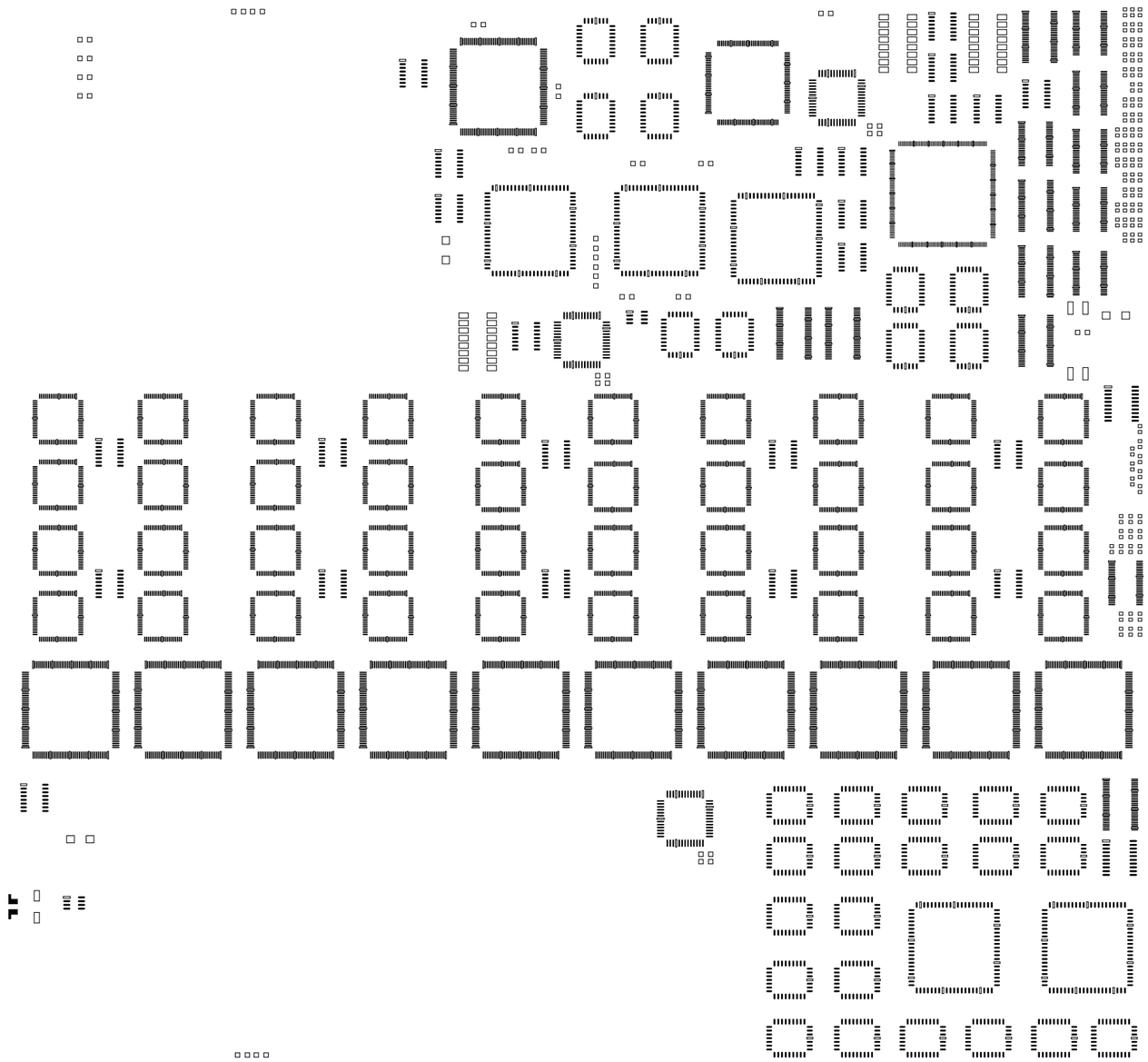
INNER TRACE 3 (VRB.IN3)

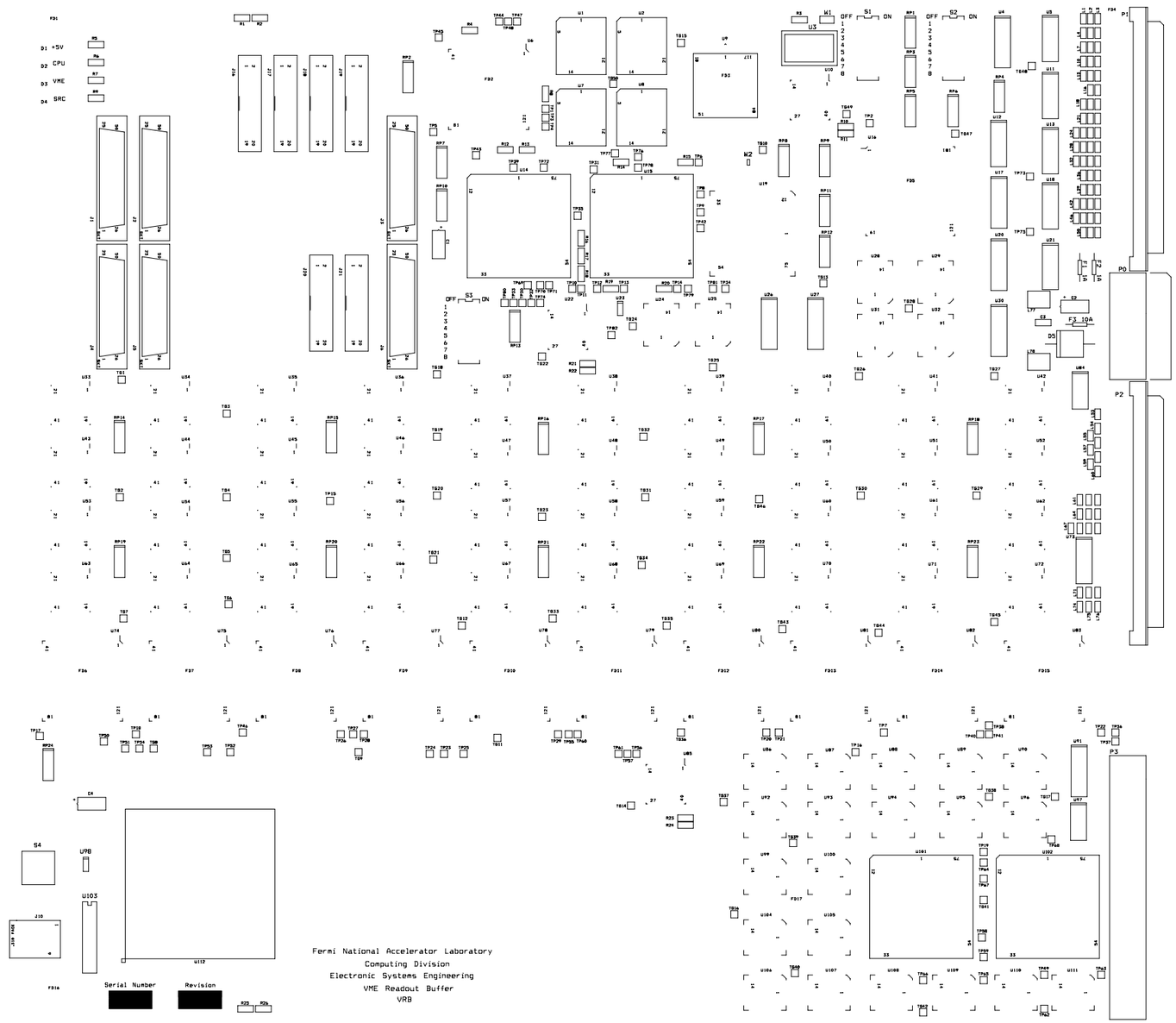
INNER TRACE 4 (VRB.IN4)



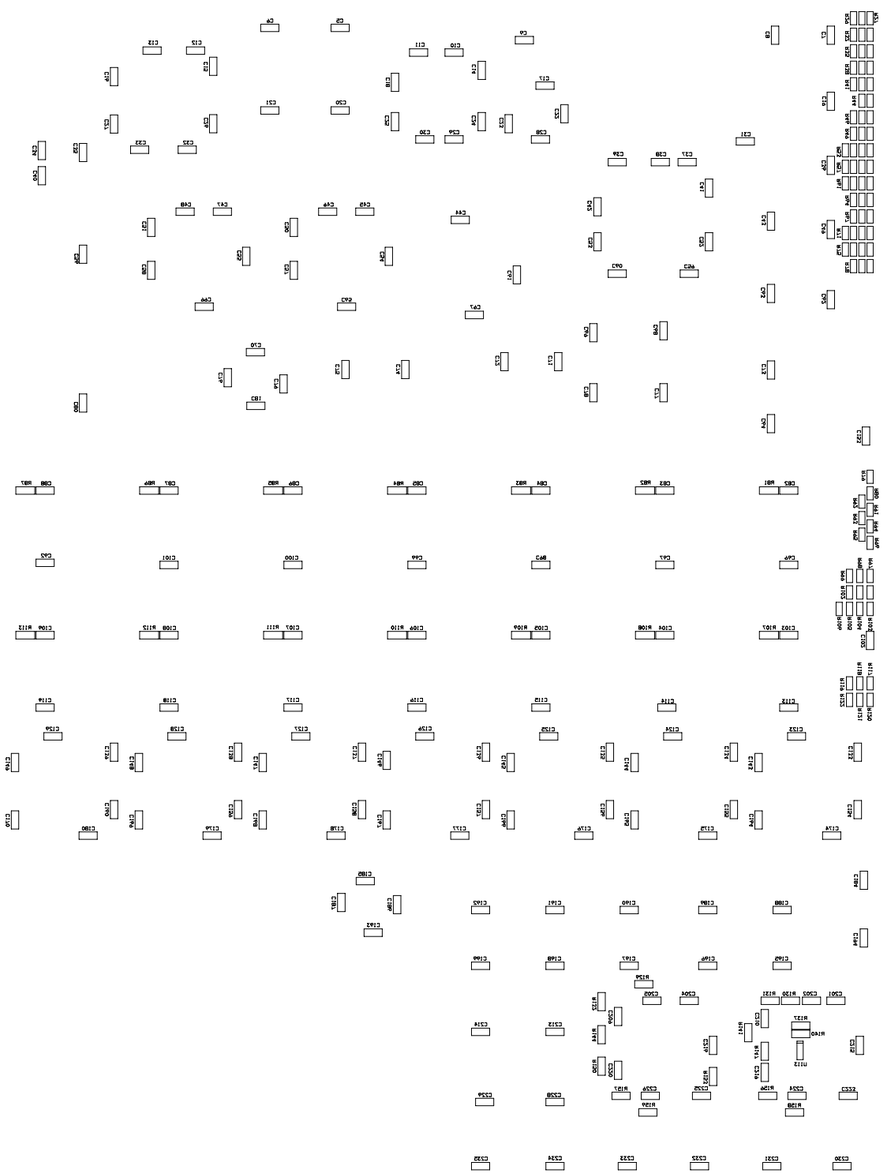
TOP SOLDER MASK

BOTTOM SOLDER MASK

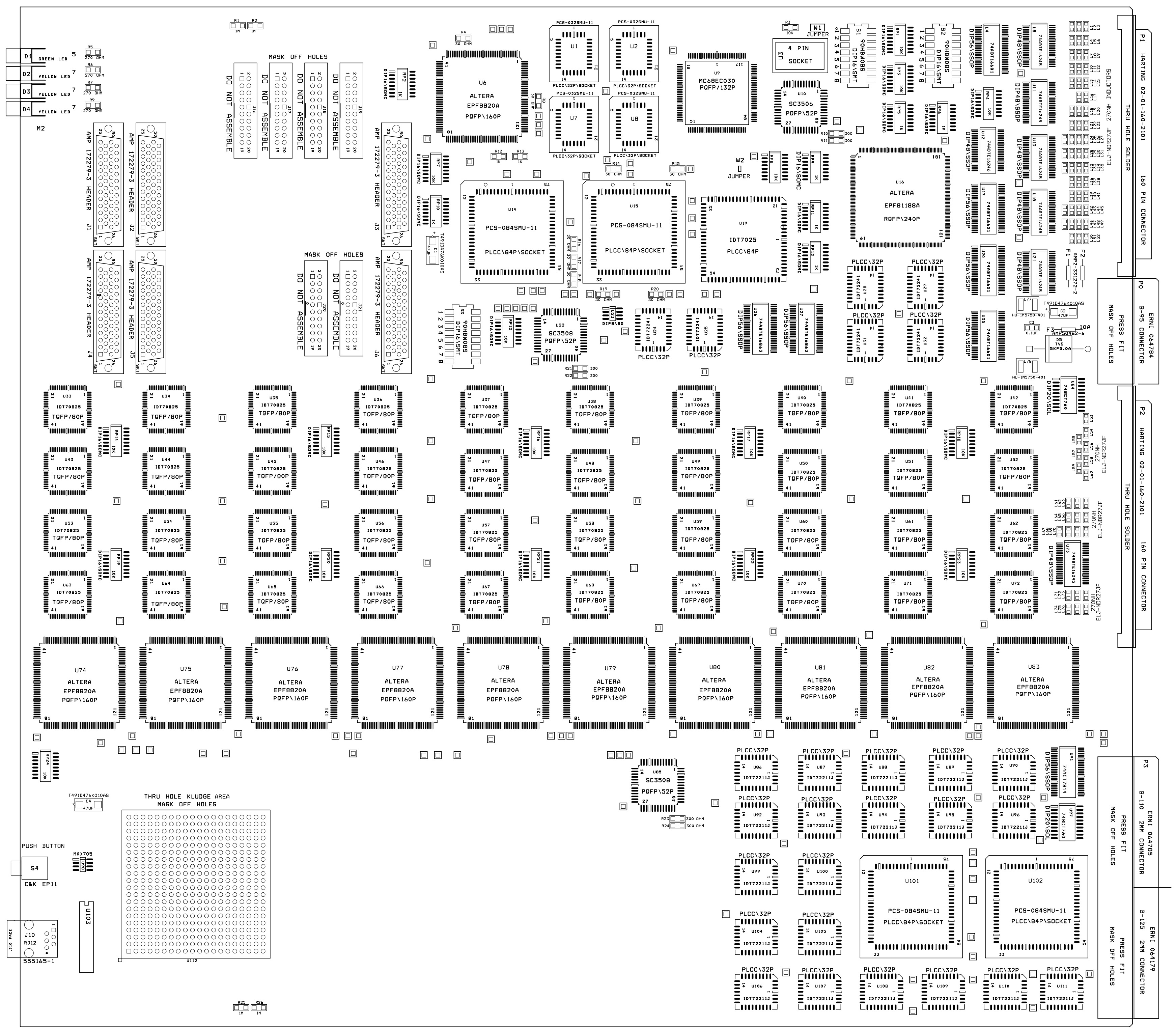




TOP SILKSCREEN



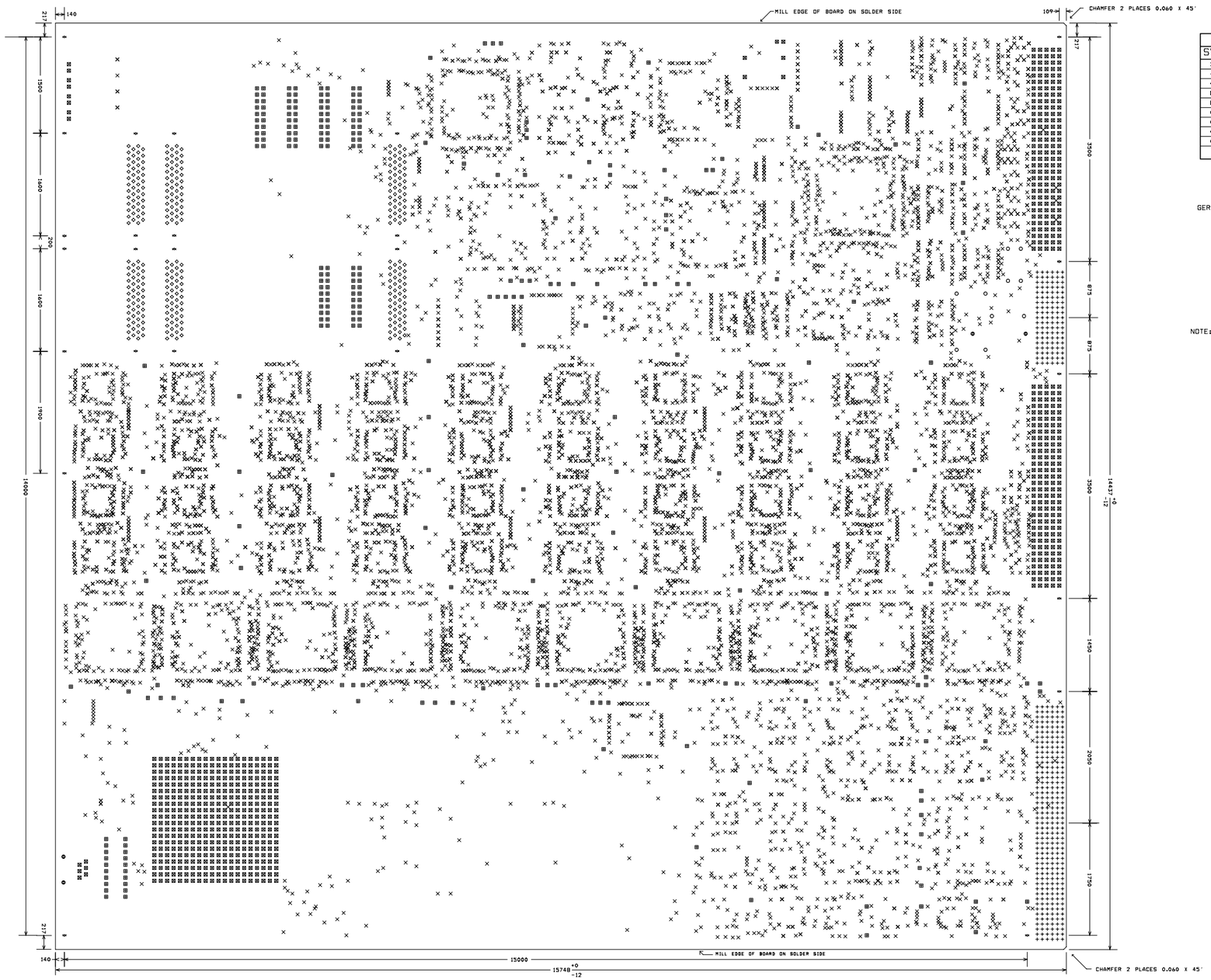
BOTTOM SILKSCREEN



NOTES:

DO NOT ASSEMBLE THE FOLLOWING COMPONENTS J16, J17, J18, J19 J20 AND J21

ALL THRU HOLE AREAS ARE TO BE MASKED OFF SO COMPONENTS CAN INSTALLED AT A LATER DATE IF NECESSARY



HOLE LEGEND				
SYM	DIAM	TOL	QTY	NOTE
×	0.012		7716	
+	0.025		396	
◇	0.030		300	
⊠	0.034		740	
⊞	0.037		274	
○	0.055		10	
⊕	0.062		2	
▭	0.102		25	
●	0.128		2	
TOTAL			9465	

GERBER FILENAME BOARD LAYER STACKING ORDER

VRB.TOP	TOP LAYER 1 = TOP LAYER	5.5 Mils
VRB.PWR	INNER LAYER 2 = POWER PLANE	9.5 Mils
VRB.IN1	INNER LAYER 3 = INNER TRACE 1	5.5 Mils
VRB.IN2	INNER LAYER 4 = INNER TRACE 2	9.5 Mils
VRB.GND	INNER LAYER 5 = GROUND PLANE	33 Mils +/- 8 Mils
VRB.PWR	INNER LAYER 6 = POWER PLANE	9.5 Mils
VRB.IN3	INNER LAYER 7 = INNER TRACE 3	5.5 Mils
VRB.IN4	INNER LAYER 8 = INNER TRACE 4	9.5 Mils
VRB.GND	INNER LAYER 9 = GROUND PLANE	5.5 Mils
VRB.BOT	BOTTOM LAYER 10 = BOTTOM LAYER	

NOTE: THIS IS A CONTROLLED IMPEDANCE PC BOARD

OVERALL PC BOARD THICKNESS = 0.093 +/- 0.008 INCH

MILL PC BOARD EDGE TO A THICKNESS OF 0.063 +/- 0.008 INCH ON SOLDER SIDE FOR A DISTANCE OF 0.098 INCH FROM THE TOP AND BOTTOM OF THE BOARD

CHAMFER FRONT 2 CORNERS OF BOARD 0.060 X 45 DEGREES

PC BOARD DIMENSIONS ARE IN MILS