MI BPM Project
MI BPM TB Controller Status Report January 17th, 2006, 9:30am

Hardware status

MI BPM_TB Controller Prototype:
-) All parts ordered and were received.
-) Assembled second board. Third board assembling is in progress (Neal WILCER).
-) One of the MI BPM TB Controller Prototypes with required hardware and interface software has been relocated to Accelerator Division (Manfred WENDT).

Interface with the MI BPM TB:
-) Started integration tests at Accelerator Division working with Manfred WENDT, Andrea SAEWERT and Neal WILCER.
Tests were run using:
1) MI BPM_TB Controller Prototype
2) Two MI BPM TBs (one digital logic only and one fully populated board)
3) Custom subrack with repositioned VME P1 Backplane.

Interface with the Timing Module:
-) Started integration tests at FCC working with Bill HAYNES and Neal WILCER. Preliminary tests successful.

Firmware status:

MI BPM_TB Firmware
-) From Status report January 3rd :
"*First release completed as specification.*
Allows for the MI_BPM_TB to be remotely controlled
It requires an external test signal."

-) From Status report January 10th: "*Work in progress on second release which provides for an on board test signal and more diagnostics.*"
Work still in progress on improvements.

MI BPM_TB Controller Prototype Firmware (Avnet Xilinx card FPGA)

-) From Status report January 3rd :
"*Implemented simplified interface with MI_BPM_TB*".

-) From Status report January 10th,: "*Work in progress on new version with more diagnostic and the capability to generate an on-board test signal and more diagnostics.*" Work still in progress on improvements.