Test Waveform Generator System
Fermilab October 21th, 2004

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TWG System status

Software:

-) Defining command sequences (IEEE-488, RS232 interfaces) to be used during system testing.

Hardware:

-) Received most of the components:

Agilent 33250A Function/Arbitrary Waveform generator
Agilent 3499A Switch/Control System, 5 slot mainframe
Agilent 44478A, Dual 1x4 Multiplexer

-) Waiting for:

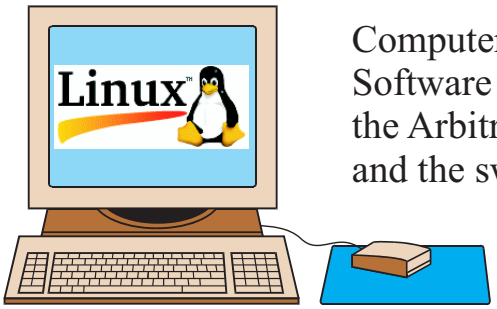
Agilent N2272A, 1x9 RF Multiplexer Module
Expected delivery is November 7, 2004.

Work in progress:

-) System components testing and system assembly.
-) Custom adapter components purchase.
-) Cable/transformers testing.

TWG Documentation (including this report):

http://www-ese.fnal.gov/D0Cal_TWG/



Computer with Linux operating system.
Software is required to control
the Arbitrary waveform generator
and the switch/control system.

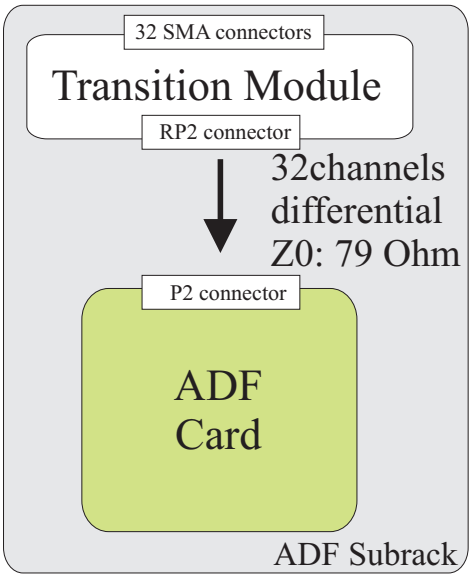
Control
IEEE488 (GPIB, HPIB), RS232

Agilent 33250A
Function/Arbitrary Waveform Generator

1channel, single ended
BNC terminated coax cable, Z0: 50 Ohm

Agilent 3499A Switch/Control System
Modules:
1 Agilent 2272A 1x9 Multiplexer Module
4 Agilent 44472A 1x4 Multiplexer Module

32 channels, single ended
BNC/SMA adapters
SMA terminated coax cables, Z0: 50 Ohm



Adapt the impedance from 50 Ohm to 79 Ohm
and convert the signals from single ended to differential.

Unit Under Test
Analog to Digital converter and Filter board.