

Test Waveform Generator System

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TWG Prototype System status

Software:

-) No relevant changes to report.

Hardware:

-) Implemented and tested SCL triggering. The system has now the capability to trigger on a “beam-crossing” or “turn” number specified by the user.
-) Implemented external (push button) and software (RS232) triggers.
-) Expanded channel memory from 1024 x 12bit words to 2048 x 12 bit words. This allows storing up to 292 beam crossings of DAC data.

Work in progress:

-) Modification of software interface to include SCL triggers/controls and take into account expanded memory.
-) Improvements in the SCL /software/external trigger controls.
-) Implementation of filtering (in the amplification stages) to limit bandwidth of output signals [Report Nov 13 2003].
-) Documentation/web page updates.

TWG System

Work in progress on:

-) Specification proposal for 32 channels system [Report Feb 19 2004].
-) Preliminary tests of USB interface to be used in 16 channels TWG board.

As always comments and ideas are very welcome.

TWG Documentation (including this report):

http://www-ese.fnal.gov/D0Cal_TWG/