

# RICH READOUT PLANS

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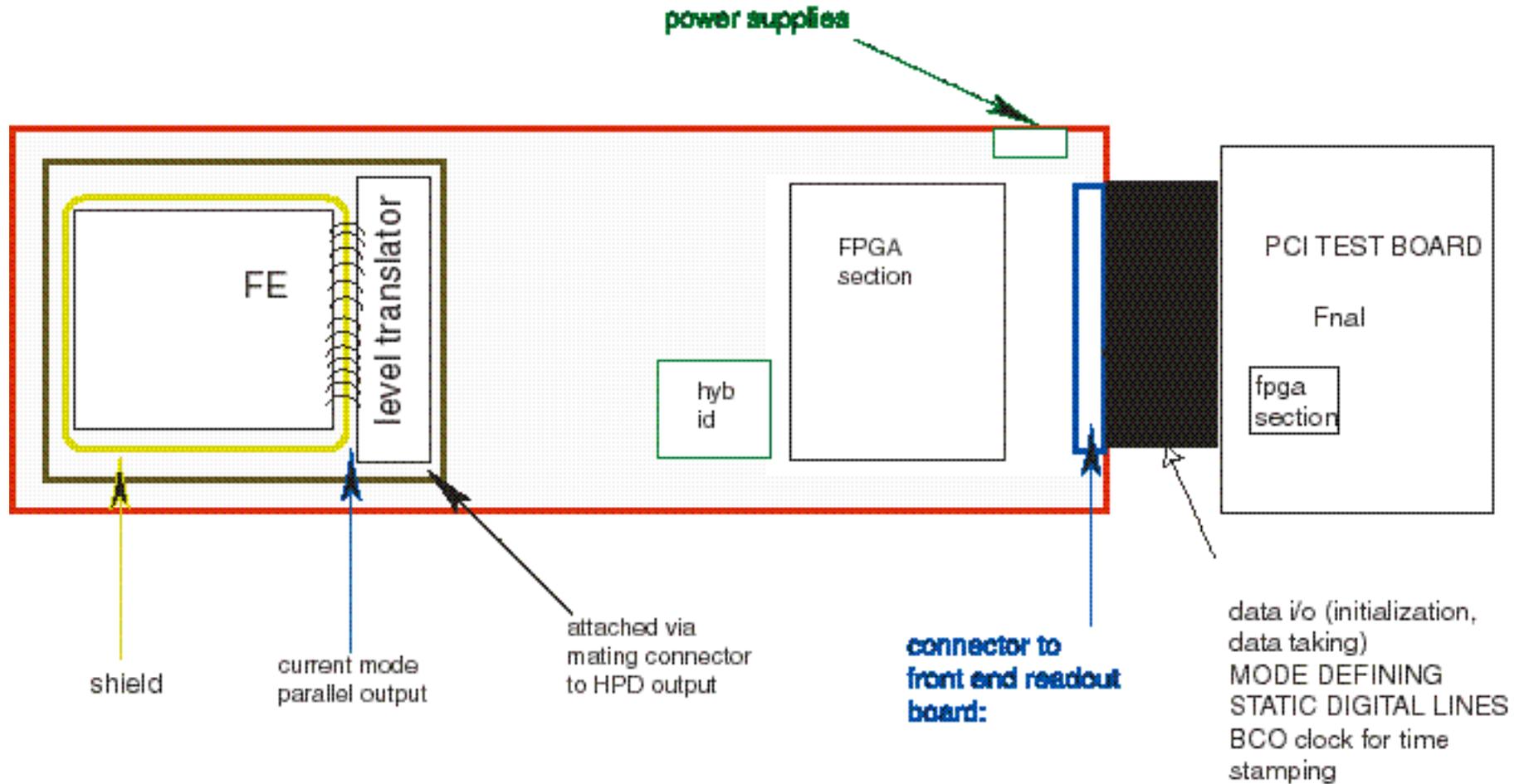
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# Milestones Towards Lehman Review:

- 1<sup>st</sup> 163 channel HPD
- 1<sup>st</sup> stage of front end electronics R&D: 2 custom made front end electronics hybrids
  - Goal: test low noise performance and develop protocol of hybrid FPGA – PCI test card interface
- HV for HPD choice and testing
- Cable choice, routing, stress relief

# The hybrid test setup



# Hybrid test set-up at Syracuse

- ◆ Module testing and infrastructure: we are eager to establish at Syracuse a prototype test system adapting the FNAL PCI test card
- ◆ We will use Labview as GUI and DAQ control tool.
- ◆ We will experiment with the best partition on FPGA functions between hybrid and DCB.

# Post Lehman review issues

- ◆ Location of front end hybrids and associated cable plant
  - ⇒ We will request changes to Chuck Brown's drawings
  - ⇒ **Mechanical engineer** support urgently needed (location of parts, support system, assembly issues)
- ◆ Power distribution and segmentation needs to be designed and tested for low noise performance