

## Session 5 Post Baseline Documentation

- 2 Systems for now

- (1) FEB (ASDQ)

- (2) Serializer (Pre-DCB “board”)

- ★ Can take advantage of any DCB that is made

-Power, HV, Thermal, Controls, Output, Receiver, Noise, Gain

- Protection

- (1) Close adherence to Fermi-standards

- (2) Cool-Polysilicon Resettable Fuses (ala D0)

- Timing

- (1) Repeatability (muon latch is jitter tolerant)

- (2) Testability / on board adjust (pulser timing needs planning)

## Session 5 Post Baseline

### All Front Ends

- Front end/ Serailizer approach
  - (1) Can be adapted for other detectors
  - (2) Serialzer is only non-standard item (can be TDC)
  - (3) Moves potenital noise source
  - (4) Quasi-Accessible for repair/maintaining
  - (5) Rack mount DCB easy to get at
  - (6) Good place to mask troubled channels
  - (7) Sparsify at Octant/view level anyway

### Robust system

- Redundant Planes, but trigger!
  - (1) We will break channels in Monte Carlo
  - (2) Have Remote Thresholds + Atten for each 8 chnls

-Determine point at which we go downstairs

# Muon system (One View of one Octant)

