

Readout and Controls

Pre-baseline requirements

- mostly simulation, no prototype hardware
- development tools for simulation, FPGA, embedded processors

Areas needing attention

- slow controls, databases

Readout Electronics

Data Combiners
Control & Timing Distribution
Optical Links
Level 1 Buffers
Event Builder (Switch)

Infrastructure & Integration

Networking
Control Room
Databases
Test Systems
Integration & Installation

Controls and Software

Partitioning
Run Control
Error Handling
Status Monitor
Cold Start
Message Passing
Control Room Log
Slow Controls
Data Logging
Detector Management
Electronics Support

Management

WBS

Original Version

- 2500 lines
- still missing important items
- unreadable (too many minor items)

Last Thursday Meeting

- breakup major tasks into ~1 month increments
=> 10,000 lines

Current

- reorganized so that all items of similar importance are at same WBS levels

WBS

1	Level 1	BTeV
1	Level 2	Readout and Controls
4	Level 3	Readout Electronics/ Controls & Software/ Infrastructure & Integration
10	Level 4	Design Phase (Prototype/Production)
60	Level 5	Major Subsystem/Task
250	Level 6	Major Subtask (Design, Manufacture, Test...)
2500	Level 7	Subtask Tracking Item (more than a few weeks effort or more than a few thousand dollars)
10,000	Level 8	Minor Detail